



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/059,644
Filing Date April 13, 1998
Inventor Pai-Hung Pan
Assignee Micron Technology, Inc.
Group Art Unit 2822
Examiner Trinh, Michael
Attorney's Docket No. MI22-898
Title: Semiconductor Processing Methods of Forming a Conductive Gate and Line

Assistant Commissioner of Patents

Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Please recognize Jennifer J. Taylor, Ph.D. (Reg. No. 48,711), Satheesh K. Karra (Reg. No. 40,246), and Robert C. Hyta (Reg. No. 46,791), whose post office address is 601 W. First Avenue, Suite 1300, Spokane, Washington 99201-3828, as an associate attorney or agent in the above-entitled application.

Date: _____

2/5/03

David G. Latwesen, Ph.D.
Reg. No. 38,533

